

Form PTO-1449
(REV. 8-83)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

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2001 P 14591 US

SERIAL NO.

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10/08/04

JC986 U.S. PTO
 10/08/04
 02/28/02

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

INTEGRATION SCHEME FOR METAL
GAP FILL, WITH FIXED ABRASIVE CMP

APPLICANT

Wrschka et al.

EXAMINER

~~Not Yet Assigned~~

Michelle Estrada

FILING DATE

February 28, 2002

GROUP

2823

~~Not Yet Assigned~~

U. S. PATENT DOCUMENTS

Examiner INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
		5,958,794	Sep. 28, 1999	Bruxvoort et al.	438	692	
		6,007,404	Dec. 28, 1999	Rutherford et al.	451	41	
		6,234,875 B1	May. 22, 2001	Pendergrass, Jr.	451	41	
		6,325,702	Dec. 5, 2001	Robinson	451	41	

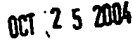
FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER		DATE CONSIDERED 11/30/04

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Approved for use through 07/31/2006. OMB 0651-0031

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

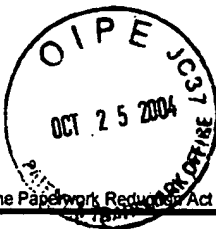
**Examiner
Signature**

Date
Considered

11/30/04

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PTO/SB/08b (08-03)
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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known			
		Application Number	10/084,194		
		Filing Date	February 28, 2002		
		First Named Inventor	Wrschka, et al.		
		Art Unit	2823		
		Examiner Name	Estrada, Michelle		
Sheet	2	of	2	Attorney Docket Number	2001 P 14591 US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
ME	11	WOLF, S., <i>Silicon Processing for the VLSI Era</i> , Vol. 2, Lattice Press, Sunset Beach, California (1990) pp. 189-193.	
ME	12	PENKA, S., et al., "Integration Aspects of Flowfill and Spin-on-Glass Process for Sub-0.35µm Interconnects," Interconnect Technology Conference, Proceedings of the IEEE 1998 International (June 1-3, 1998) pp. 271-273.	
ME	13	MENDONCA, J., et al., "Interconnect Material and CMP Process Change Effects on Local Interconnect Planarity," Interconnect Technology Conference, Proceedings of the IEEE 1998 International (1998) pp. 196-198.	

Examiner Signature	Michelle Estrada	Date Considered	11/30/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
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